

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An oscillator circuit, at least comprising:
at least one oscillator device having at least one oscillator bias contact;
~~a bias source having a source contact connected to said oscillator bias contact, and~~
a signal shaper device ~~connected to an output of the oscillator device, said signal shaper device being arranged for clipping the amplitude of the output signal of the oscillator device to a predetermined amplitude; outputting a limited output signal with a constant amplitude when the amplitude of the a signal provided by the oscillator device is above a boundary value;~~
~~a bias source having a source contact connected to said oscillator bias contact;~~
said bias source having a first state in which a high level of energy is provided at said source contact, and ~~[[,]]~~ a second state in which a lower level of energy is provided at said source contact, wherein the high level of energy at said source contact will keep the oscillator device in an oscillating state, whereas the lower level of energy will result in a decreasing ~~amplitude~~ of the signal provided by the oscillator device; and ~~said circuit further including; wherein~~
~~at least one switch~~ switching means are provided for switching the bias source from the first state to the second state ~~when a predetermined criterion is satisfied and and~~ from the second state back to the first state before the decreasing amplitude of the signal provided by the oscillator device decreases below ~~the boundary value~~ ~~amplitude at which the limiter device stops functioning properly.~~

2. (Previously Presented) An oscillator circuit as claimed in claim 1 wherein said bias source is a switched DC source which in use provides a bias signal varying between a first level and a second level.

3. (Previously Presented) An oscillator circuit as claimed in claim 1, wherein the lower level of energy is substantially zero.

4. (Currently Amended) An oscillator circuit as claimed in claim 3~~1~~, wherein said signal shaper device comprises at least one limiter device, said limiter device outputting an output signal with an amplitude proportional to said amplitude of the signal provided by the oscillator device when said amplitude of the signal provided by the oscillator device is below said boundary value.

5. (Currently Amended) An oscillator circuit as claimed in claim 8~~1~~, wherein said signal shaper device comprises a band-pass filter device.

6. (Previously Presented) An oscillator circuit as claimed in claim 1, further comprising:

a bias control circuit for switching the bias source on and off depending on a signal outputted by the oscillator device.

7. (Currently Amended) An oscillator circuit as claimed in claim 1, wherein said oscillator device at least comprises at least one electrical ~~devices-device~~ with a positive feedback loop.

8. (Previously Presented) An oscillator circuit as claimed in claim 1, wherein said oscillator device at least comprises at least one resonator body.

9. (Currently Amended) An oscillator circuit as claimed in claim 1, further comprising a negative resistance device ~~at least comprising~~ at least one transistor device.

10. (Currently Amended) An oscillator circuit as claimed in claim 4, wherein said limiter ~~at least device~~ comprises at least one differential amplifier with ~~[[.]]~~ at least one input contact connected to at least one oscillator output contact, and at least one output contact connected to a load.

11. (Previously Presented) An oscillator circuit as claimed in claim 10, wherein said load comprises:

at least one resistor connecting at least one of said at least one output contacts to a power supply.

12. (Currently Amended) An oscillator circuit as claimed in -claims 4, wherein said limiter at least comprises at least one ~~translator~~ transistor device.

13. (Previously Presented) An oscillator circuit as claimed in claim 1, wherein said bias source comprises a bias voltage source.

14. (Previously Presented) An oscillator circuit as claimed in claim 1, wherein said bias source comprises a bias current source.

15. (Previously Presented) A wireless electronic device including an oscillator circuit as claimed in claim 1.

16. (Currently Amended) A method for reducing the power consumption of an oscillator circuit,

~~including an oscillator device and a bias source, said method least using a bias source having a first state in which a high level of energy is provided to the oscillator circuit and a second state in which a lower level of energy or no energy is provided to the oscillator circuit, wherein the bias source, wherein the bias source is switched from the first state to the second state if a first predetermined criterion is satisfied and wherein the bias source is switched back to the first state if a predetermined second criterion is satisfied, said oscillator circuit including an oscillator device and a bias source, said method including:~~

~~outputting a limited output signal with a constant amplitude when the amplitude of the signal provided by the oscillator device is above a boundary value;~~

~~switching when a predetermined criterion is satisfied said bias source from a first state in which a high level of energy is provided to the oscillator circuit to a second state in which a lower level of energy is provided to the oscillator circuit, wherein the high level of energy at said source contact will keep the oscillator device in an oscillating state, whereas the lower level of energy will result in a decreasing amplitude of the signal provided by the oscillator device the first state to the second state; and~~

~~switching the bias source from the second state back to the first state before the decreasing amplitude of the signal provided by the oscillator device decreases below the boundary value.~~